

"Express Mail" Label No. EL525739190US
Date of Deposit October 30, 2001

Attorney Docket No.: 16869P-037300US
Client Reference No.: 210000604US1

PATENT

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jc971 U.S. PTO
10/004168
10/30/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Makoto Ono, et al.

Application No.:

Filed: Herewith

For: Inspection System and
Semiconductor Device Manufacturing
Method

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 and §1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The references cited on attached form PTO/SB/08A are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement, since it is being submitted prior to the first Office Action. However, if a fee is required, the

Makoto Ono, et al.
Application No.:
Page +

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Commissioner is authorized to deduct such fee from Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



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